

Attorney's Docket No. 5649-877



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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Cho et al.  
Serial No.: 09/923,670  
Filed: August 7, 2001  
For: INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS  
PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES AND  
METHODS OF FABRICATING SAME

Group Art Unit: 2836  
Examiner: To Be Assigned

Date: June 24, 2002

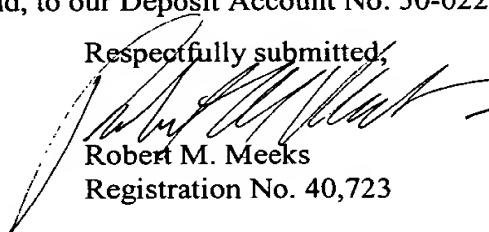
Commissioner for Patents  
Washington, DC 20231

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a Supplemental Form PTO-1449 listing several documents cited in the Notice to Submit Response for the corresponding Korean Application Number 10-2000-0046615. Each document listed on the attached PTO-1449 was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement. A copy of each document, including the Notice, is enclosed. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Respectfully submitted,

  
Robert M. Meeks  
Registration No. 40,723



20792  
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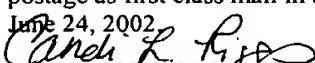
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on

June 24, 2002

  
Candi L. Riggs; Date of Signature: June 24, 2002